

SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

PRODUCT SPECIFICATION

規格書

CUSTOMER : CBS DATE : 2018-04-20

(客戶): (日期):

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : KM 400V100μF(φ16X30)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLI	ER
PREPARED (拟定)	CHECKED (审核)
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CUS	TOMER
APPROVAL	SIGNATURE
(批准)	(签名)

ELECTROLYTIC CAPACITOR SPECIFICATION KM SERIES

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Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

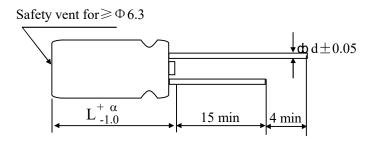
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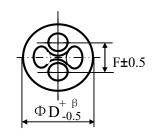
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Table 1 Product Dimensions and Characteristics

Unit: mm





α	L<20 : α=1.5; L≥20 : α=2.0
β	$\Phi D < 20 : \beta = 0.5; \Phi D \ge 20 : \beta = 1.0$

* If it is flat rubber, there is no bulge from the flat rubber surface.

N	SAMXON	WV	Cap.	Cap. tolerance	Temp.	tanδ (120Hz,	Leakage Current	Max Ripple Current at 105°C	Load lifetim		ension mm)		Sleeve
0.	Part No.	(Vdc)	(μF)	1	range(°C)	20°C)	(µA,2min)	120Hz (Arms)	e (Hrs)	$D \times L$	F	фd	
1	EKM107M2GK30RRS9P	400	100	-20%~+20%	-25~105	0.24	1240	0.58	2000	16X30	7.5	0.8	PET

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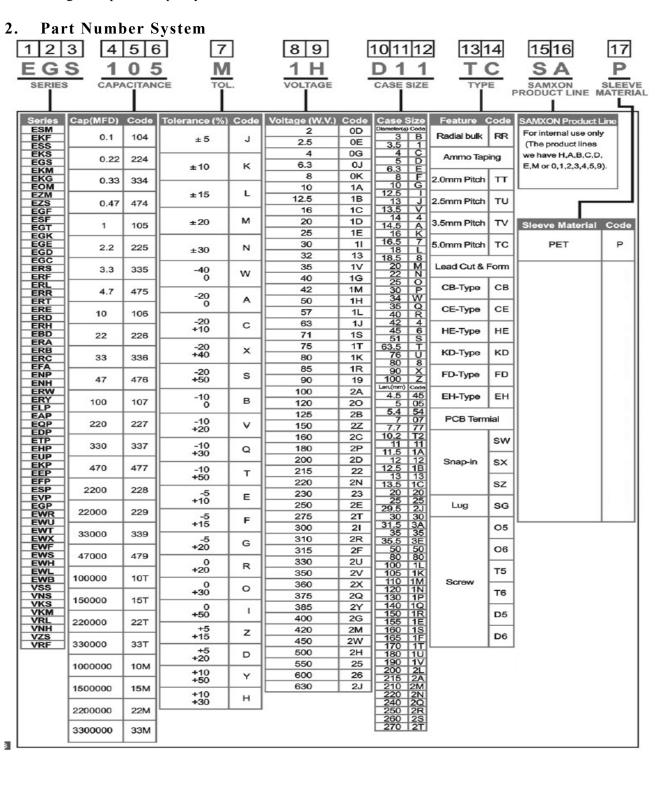
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1. Application

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment. Designed capacitor's quality meets IEC60384.

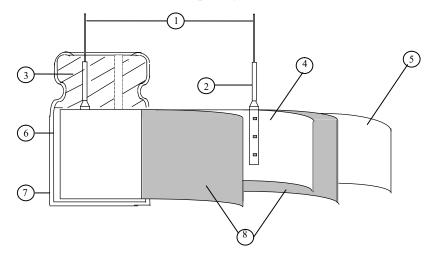


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3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

4. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature $: 20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PE	RFOR	MANC	Е			
	Rated voltage	WV (V.DC)	6.3	10	1	6	25	35	50	63	100
	(WV)	SV (V.DC)	8	13	2	0	32	44	63	79	125
4.1		WW (W DC)	160	200	220	250	250	400	120	450	
	Surge voltage (SV)	WV (V.DC) SV (V.DC)	160 200	200	220 270	300	350 400	400	420	500	
4.2	Nominal capacitance (Tolerance)	<pre><condition> Measuring F Measuring V Measuring T </condition></pre> <pre><criteria> Shall be with</criteria></pre>	requend oltage empera	: uture :	Not m 20±2	\mathbb{C}	nn 0.5V				
4.3	Leakage current	<pre><condition> Connecting t minutes, and <criteria> Refer to Tabl</criteria></condition></pre>	the capa		-			stor (1	kΩ±1	0Ω) in s	series for
4.4	tan δ	<pre><condition> See 4.2, Nor </condition></pre> <pre><criteria> Refer to Tabl</criteria></pre>	т Сара	citance,	for me	easurin	g frequ	ency, vo	ltage ar	nd temper	ature.
4.5	Terminal strength	0.51	ength ocapacitor rength ocapacitor 2~3 sec	or, applied of Termine, applied onds, ar	nals. If force the the the the the the the the the th	to ben bent in Fensile (k 5 (l	t the ter	rminal (1	l~4 mm original Bendin (1 2.5	from the	rubber) i

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		<condition></condition>								
		STEP	Testi	ng Tempe	rature(°C)			Time		
		1		20±2	2	Time	to read	ch thermal ed	quilibriu	m
		2		-40(-25)	±3	Time	to read	ch thermal ed	quilibriu	m
		3		20 ± 2				ch thermal ed	•	
		4		105±		+		ch thermal ed	•	
		5		$\frac{103\pm}{20\pm2}$		_		ch thermal ed	•	
				20±2	<u> </u>	Time	to reac	in thermal ed	<u> </u>	m
		<criteria></criteria>	21	. 41 1.	'. CT.	1.4701 1	1	4	1.1	11 4
		a. tan δ shall b				1.41 ne 16	eakage	current mea	surea sn	an not
	Temperature	more than 8 tim				C T4	. 4 4T	V 1 1		1 11 4
	characteristi	b. In step 5, ta			nin the iim	it of Iter	1 4.41	ne leakage c	current s	naii not
4.6	cs	more than the s	-		(_)4:	11 4		1 41 1 4	C41 C-11	
		c. At-40°C (-25 table.	5 C), 11	mpedance	(z) rauo s	naii not (exceed	the value of	the follo	owing
		Working Voltag	ge (V)	6.3	10	16	25	35	50	63
		Z-25°C/Z+20	°C	5	4	3	2	2	2	2
		Z-40°C/Z+20	°C	10	8	6	4	3	3	3
		Working Voltage	e (V)	100	160~220	250~	350	400~420	450	
		Z-25°C/Z+20		2	3	4		6	15	
		Z-40°C/Z+20		3			_			
				_						
		For capacitance	value	> 1000 11	$F \Delta dd 0.5$	ner ano	ther 10	000 H F for 7	7-25/7+2	$^{\circ}$ 0°C
		For capacitance	value	> 1000 µ						
		For capacitance Capacitance, tan			Add 1.0	per anot	her 10	000 μ F for Z		
					Add 1.0	per anot	her 10	000 μ F for Z		
		Capacitance, tan	ιδ, and	d impedar	Add 1.0 nce shall be	per anot	her 10 ed at 1	000 μ F for Z 20Hz.	-40°C/Z-	+20℃.
		Capacitance, tan <condition> According to IE</condition>	ιδ, and EC6038	d impedar 34-4No.4.	Add 1.0 nce shall be	per anote measures, The ca	her 10 ed at 1 pacito	000 μ F for Z 20Hz. r is stored at	-40°C/Z	+20°C.
		Capacitance, tan	δ, and EC6038 DC bi	d impedar 34-4No.4. as voltage	Add 1.0 nce shall be 13 method e plus the ra	per anote measures, The ca	her 10 ed at 1 pacito le curr	000 μ F for Z 20Hz. r is stored at tent for Tabl	a tempe e 1. (Th	+20°C. rature of e sum of
		Capacitance, tan Condition> According to IE 105°C ±2 with	EC6038 DC bit peak	d impedar 34-4No.4. as voltage voltage sh	Add 1.0 nce shall be 13 method e plus the ranall not ex	e measures, The cauted ripp	her 10 ed at 1 pacito le curr rated	2000 µ F for Z 20Hz. r is stored at tent for Tabl	a tempe e 1. (Th oltage) ☐	+20°C. rature of e sum of then the
	Load	Capacitance, tan <condition> According to IE 105°C ±2 with DC and ripple</condition>	δ, and EC6038 DC bit peak she tested	d impedar 34-4No.4. as voltage voltage shed after 16	Add 1.0 nce shall be 13 method e plus the ranall not ex 6 hours rec	e measures, The cauted ripp	ther 10 ed at 1 pacito le curre rated	2000 µ F for Z 20Hz. r is stored at tent for Tabl	a tempe e 1. (Th oltage) ☐	+20°C. rature of e sum of then the
4.7	Load life	Capacitance, tan <condition> According to IE 105°C ±2 with DC and ripple product should result should me <criteria></criteria></condition>	EC6038 DC bit peak be tested eet the	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the ranall not ex 6 hours rec g table:	s, The ca	pacito le curr rated ime at	20Hz. r is stored at tent for Table working vor atmospheric	a tempe e 1. (Th oltage) ☐	+20°C. rature of e sum of then the
4.7		Capacitance, tan <condition> According to IE 105°C ±2 with DC and ripple product should result should me</condition>	EC6038 DC bit peak be tested eet the	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the ranall not ex 6 hours rec g table:	s, The ca	pacito le curr rated ime at	20Hz. r is stored at tent for Table working vor atmospheric	a tempe e 1. (Th oltage) ☐	+20°C. rature of e sum of then the
4.7	life	Capacitance, tan <condition> According to IE 105°C ±2 with DC and ripple product should result should me <criteria></criteria></condition>	EC6038 DC bit peak to be testiceet the	d impedar 34-4No.4. as voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the ranall not ex 6 hours rec g table:	e measures, The canted ripp ceed the overing to	pacito le curr rated ime at	20Hz. r is stored at tent for Table working vor atmospheric	a tempe e 1. (Th oltage) ☐	+20°C. rature of e sum of then the
4.7	life	Capacitance, tan Condition> According to IE 105°C ±2 with DC and ripple product should result should me Criteria> The characteris	EC6038 DC bi peak y be teste eet the stic sha	d impedar 34-4No.4. as voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the rall not ex 6 hours rec g table:	s, The canted ripp ceed the overing to g require	pacito le curre rated ime at	20Hz. r is stored at tent for Table working vor atmospherical street.	a tempe e 1. (Th oltage) ☐	+20°C. rature of e sum of then the
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4.7	life	Capacitance, tan Condition> According to IE 105°C ±2 with DC and ripple product should result should result should me Criteria> The characteris Leakage Capacita tan δ Appearan	EC6038 DC bit peak be testiceet the current cu	d impedar 34-4No.4. as voltage shed after 16 following Ill meet that the mange	Add 1.0 nce shall be 13 method e plus the ranall not ex 5 hours rece table: e following Value in 4 Within ± Not more There sha	s, The canted ripp ceed the overing to the form and the form of th	pacito le curre rated iments be sat initial 0% of leakag	2000 µ F for Z 20Hz. r is stored at tent for Tabl working vo atmospheric isfied value. the specified te of electroly	a tempe e 1. (The oltage) To condition	rature of e sum of then the ons. The
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4.7	life test	Capacitance, tan Condition> According to IE 105°C ±2 with DC and ripple product should in result should me Criteria> The characteris Leakage Capacita tan δ Appearan Condition> The capacitors an 1000+48/0 hour chamber and be shall be connect applied for 30m	EC6038 DC bit peak be testice the stic share current c	d impedar 34-4No.4. as voltage shed after 16 following Ill meet that thange stored will lowing this yed to stal a series	Add 1.0 nee shall be 13 method e plus the ranall not ex 6 hours receptable: e following Value in 4 Within ± Not more There shall the no voltate is period the bilized at ralimiting receptable.	s, The canted ripp ceed the overing to the covering to the cov	pacito le curre rated iments be sat initial 0% of leakag ed at a tors sh peratu ± 100	on property From Z 20Hz. The isstored at the ent for Table working we atmospherical atmospherical temperature and the end of the en	a tempe e 1. (The oltage) The condition of 105 decoration of 105 d	rature of e sum of Then the ons. The
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	life test Shelf life	Capacitance, tan Condition> According to IE 105°C ±2 with DC and ripple product should in result should me Criteria> The characteris Leakage Capacita tan δ Appearan Condition> The capacitors an 1000+48/0 hour chamber and be shall be connect applied for 30m	EC6038 DC bit peak be testice the stic share current c	d impedar 34-4No.4. as voltage shed after 16 following Ill meet that thange stored will lowing this yed to stal a series	Add 1.0 nee shall be 13 method e plus the ranall not ex 6 hours receptable: e following Value in 4 Within ± Not more There shall the no voltate is period the bilized at ralimiting receptable.	s, The canted ripp ceed the overing to the covering to the cov	pacito le curre rated iments be sat initial 0% of leakag ed at a tors sh peratu ± 100	on property From Z 20Hz. The isstored at the ent for Table working we atmospherical atmospherical temperature and the end of the en	a tempe e 1. (The oltage) The condition of 105 decoration of 105 d	rature of e sum of then the ons. The

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		<criteria></criteria>	
		The characteristic shall meet t	
	-1 10	Leakage current	Value in 4.3 shall be satisfied
4.0	Shelf	Capacitance Change	Within $\pm 20\%$ of initial value.
4.8	life	tan δ	Not more than 200% of the specified value.
	test	Appearance	There shall be no leakage of electrolyte.
		Remark: If the capacitors are	stored more than 1 year, the leakage current may
		increase. Please apply voltage	e through about 1 k Ω resistor, if necessary.
		11 0 0	
		C _R :Nominal Capacitance (
		<pre><criteria></criteria></pre>	
4.9	Surge	Leakage current	Not more than the specified value.
	test	Capacitance Change	Within $\pm 15\%$ of initial value.
		tan δ	Not more than the specified value.
		Appearance Attention:	There shall be no leakage of electrolyte.
		This test simulates over voltage as often applied	ge at abnormal situation only. It is not applicable to such.
4.10	Vibration test	perpendicular directions. Vibration frequency ra Peak to peak amplitude Sweep rate Mounting method:	: 1.5mm : $10\text{Hz} \sim 55\text{Hz} \sim 10\text{Hz}$ in about 1 minute greater than 12.5mm or longer than 25mm must be fixed Within 30°
		Appearance of	tems shall be tested: No intermittent contacts, open or short circuiting. No damage of tab terminals or electrodes. No mechanical damage in terminal. No leakage of electrolyte or swelling of the case. The markings shall be legible.

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		<condition></condition>				
		The capacitor shall be tes	sted under the following	conditions:		
		Soldering temperature	: 245±3°C			
		Dipping depth	: 2mm			
4.11	Solderability	Dipping speed	: 25±2.5mr	m/s		
	test	Dipping time	$: 3\pm 0.5s$			
		<criteria></criteria>	1	2072/ 21 2 1 1		
		Coating quality	A minimu immersed	m of 95% of the surface being		
		<condition></condition>				
		Terminals of the capacito	r shall be immersed in	to solder bath at $260\pm5^{\circ}\mathrm{C}\mathrm{for}10\pm$		
		1 seconds or $400 \pm 10^{\circ}\text{C}$ for	or 3^{+1}_{0} seconds to 1.5~2.	0mm from the body of capacitor.		
			· ·	temperature and normal humidity		
	Resistance to	for 1~2 hours before mea		temperature and normal numberey		
4.12	solder heat	<criteria></criteria>				
	test	Leakage current	Not more than	the specified value.		
		Capacitance Change	Within ±10%	of initial value.		
		tan 8	Not more than	the specified value.		
		Appearance		no leakage of electrolyte.		
		<condition></condition>	1' / IEC(0204 4NI	47 4 1 2 1 111		
				o.4.7methods, capacitor shall be		
		placed in an oven, the condition according as be Temperature		Time		
			emperature			
		(1)+20°C		≤ 3 Minutes		
	Change of	(2)Rated low temper	rature (-40°C) (-25°C)	30±2 Minutes		
4.13	temperature	(3)Rated high tempe	rature (+105°C)	30 ± 2 Minutes		
	test	(1) to $(3)=1$ cycle, to	otal 5 cycle			
		<criteria></criteria>				
		The characteristic shall n				
		Leakage current	Not more than the	•		
		tan 8	Not more than the			
		Appearance	There shall be no l	eakage of electrolyte.		
		<condition></condition>				
		Humidity Test:	4NI 4 10 41 1	4 1 111 1 1 500 1 0		
		C		acitor shall be exposed for 500 ± 8		
		_		$2^{\circ}\mathbb{C}$, the characteristic change shall		
		meet the following requires < Criteria >	ement.			
	Damp heat	Leakage current	Not more than the spe	ecified value		
4.14	test	Capacitance Change	-	nore than the specified value. In $\pm 20\%$ of initial value.		
	tost	tan δ		of the specified value.		
		Appearance	There shall be no leal			
		Търошинос	There shall be no lear			

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4.15	Vent test	with vent. D.C. test The capacitor is current selected <table 3=""> Diameter (m 22.4 or les Over 22.4 <criteria> The vent shall op</criteria></table>	ss 1	arity re	versed	to a DC	power	source.	Γhen ε
		at 120Hz and o Table-1 The combined	permissible ripple currean be applied at maxing value of D.C voltage and shall not reverse volutipliers: Coefficient Freq. (Hz) Cap.(µF)	num op	erating	tempera	ature		eed the
4.16	Maximum permissible 4.16 (ripple current)	6.3~100	~47 68~470 ≥560 0.47~220 ≥270	0.75 0.80 0.85 0.80 0.90	1.00 1.00 1.00 1.00 1.00	1.35 1.23 1.10 1.25 1.10	1.57 1.34 1.13 1.40 1.13	2.00 1.50 1.15 1.60 1.15	

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances
	Cadmium and cadmium compounds
Heavy metals	Lead and lead compounds
Ticavy metais	Mercury and mercury compounds
	Hexavalent chromium compounds
	Polychlorinated biphenyls (PCB)
Chloinated	Polychlorinated naphthalenes (PCN)
organic	Polychlorinated terphenyls (PCT)
compounds	Short-chain chlorinated paraffins(SCCP)
	Other chlorinated organic compounds
D 1	Polybrominated biphenyls (PBB)
Brominated .	Polybrominated diphenylethers(PBDE) (including
organic	decabromodiphenyl ether[DecaBDE])
compounds	Other brominated organic compounds
Tributyltin comp	ounds(TBT)
Triphenyltin com	npounds(TPT)
Asbestos	
Specific azo com	pounds
Formaldehyde	
Beryllium oxide	
Beryllium copp	er
Specific phthalat	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)
Hydrofluorocarb	on (HFC), Perfluorocarbon (PFC)
Perfluorooctane s	sulfonates (PFOS)
Specific Benzotr	iazole

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Attachment: Application Guidelines

1.Circuit Design

1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- 1) Effects of operating temperature on electrical parameters
 - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
 - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
 - a) At higher frequencies capacitance and impedance decrease while tanδ increases.
 - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

(1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

(2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

(3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

(4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

1.4 Using Two or More Capacitors in Series or Parallel

(1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

(2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

1.5 Capacitor Mounting Considerations

(1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

(2) Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

(3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

(4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

(5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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(6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about 1kΩ.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately $1k\Omega$.
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

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- 2.6 Capacitor Handling after Solder
- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60°C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a 1000Ω , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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